

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner Geyer, Scott B.
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Title: Methods of Forming Semiconductor Constructions

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References –See Attached Form PTO/SB/08a

The attached form PTO/SB/08a is submitted in compliance with 37 CFR §1.56. Copies of the cited art are included with the exception of U.S. patents and published U.S. applications (1276 OG 55). No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: November 15, 2006

By: /Jennifer J. Taylor/
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